



“*docendo discimus*”



# Hands on Digital Prototyping - Summer School -

Universidad de Granada (UGR)  
September 27<sup>th</sup> – October 8<sup>th</sup>, 2010, Granada, Spain

## Lecturers and Courses:



**Prof. Dr.-Ing. Peter Glösekötter**  
Münster University of Applied Sciences,  
Germany

⇒ *Embedded Systems*



**Prof. D. Phil. Martin Poppe**  
Münster University of Applied Sciences,  
Germany

⇒ *Soft Core Processor*

The summer school comprehends lectures of interrelated lessons, i. e. *FPGA, Rapid Prototyping and Embedded Systems*, which jointly present a detailed and actual overview on the addressed subject, i. e. *Rapid Prototyping*. The lectures also give an outlook on Wireless Sensor Networks and energy harvesting applications. The educational focus is aimed at Master students and/or PhD students in *Electrical Engineering, Physics, Information Technology, Computer Science, or Applied Materials Research*. Basic VHDL skills will be beneficial, but are not mandatory. The summer school is optional to the regular lectures presented at the UGR. The teaching units include lectures, labs and exercises. There will be a written test at the end.

Students being interested to join our summer school in Granada are obliged to submit an application to the local organizer at the UGR in Granada.

Submission Deadline: September 24<sup>th</sup>, 2010

The number of participants is restricted to 20 students; the organizers will make a selection from incoming applications. The students selected as participants will be informed by September 26<sup>th</sup> 2010.

## Local Organization and further Information:



**Prof. Dr.-Ing. Ignacio Rojas**

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